

Prior Art

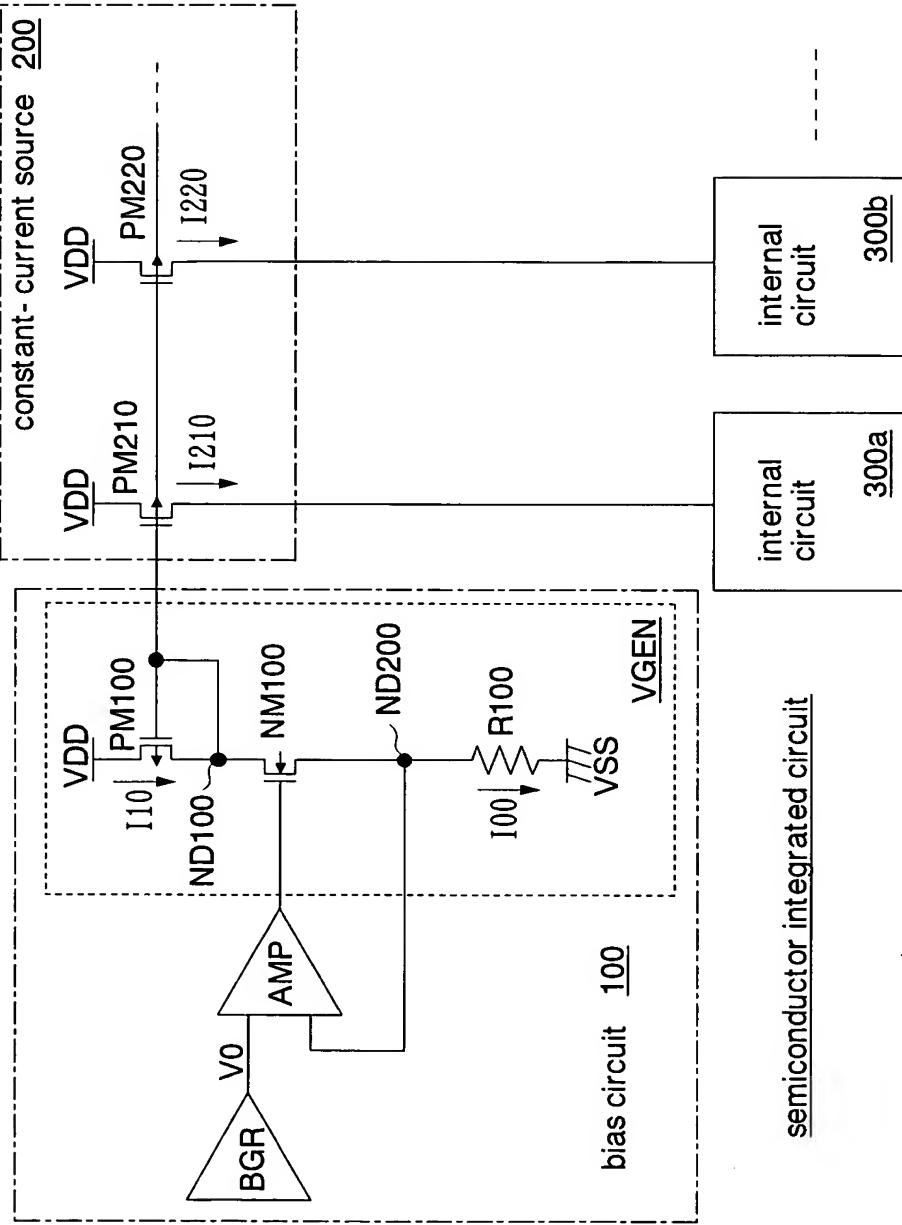


Fig. 1

Prior Art

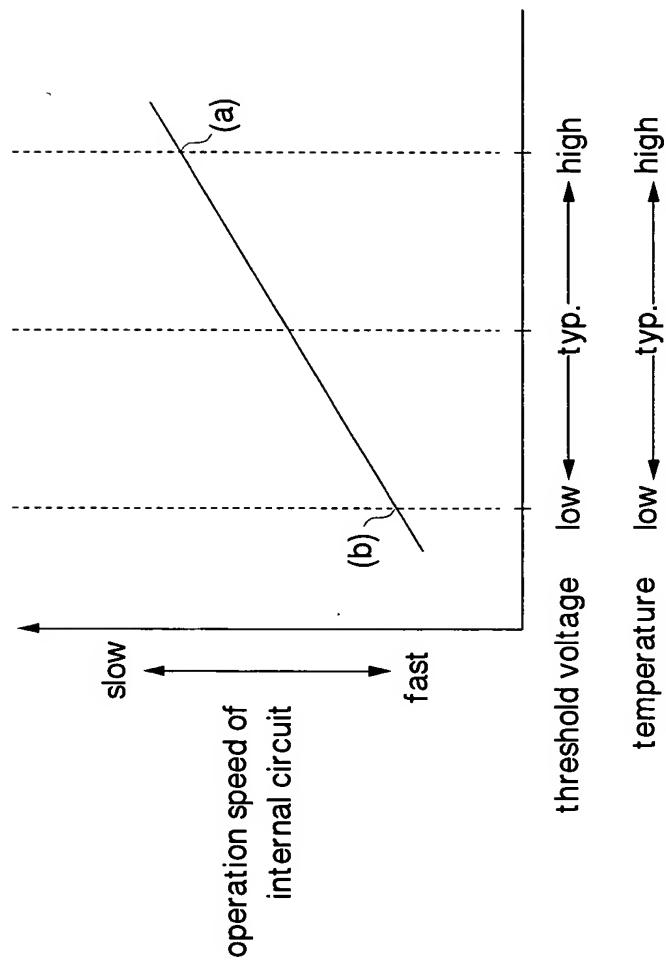


Fig. 2

Prior Art

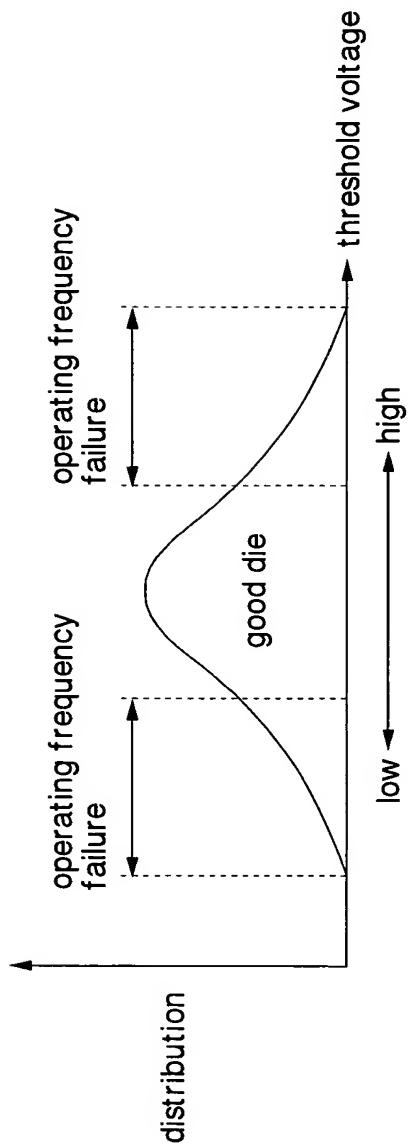


Fig. 3

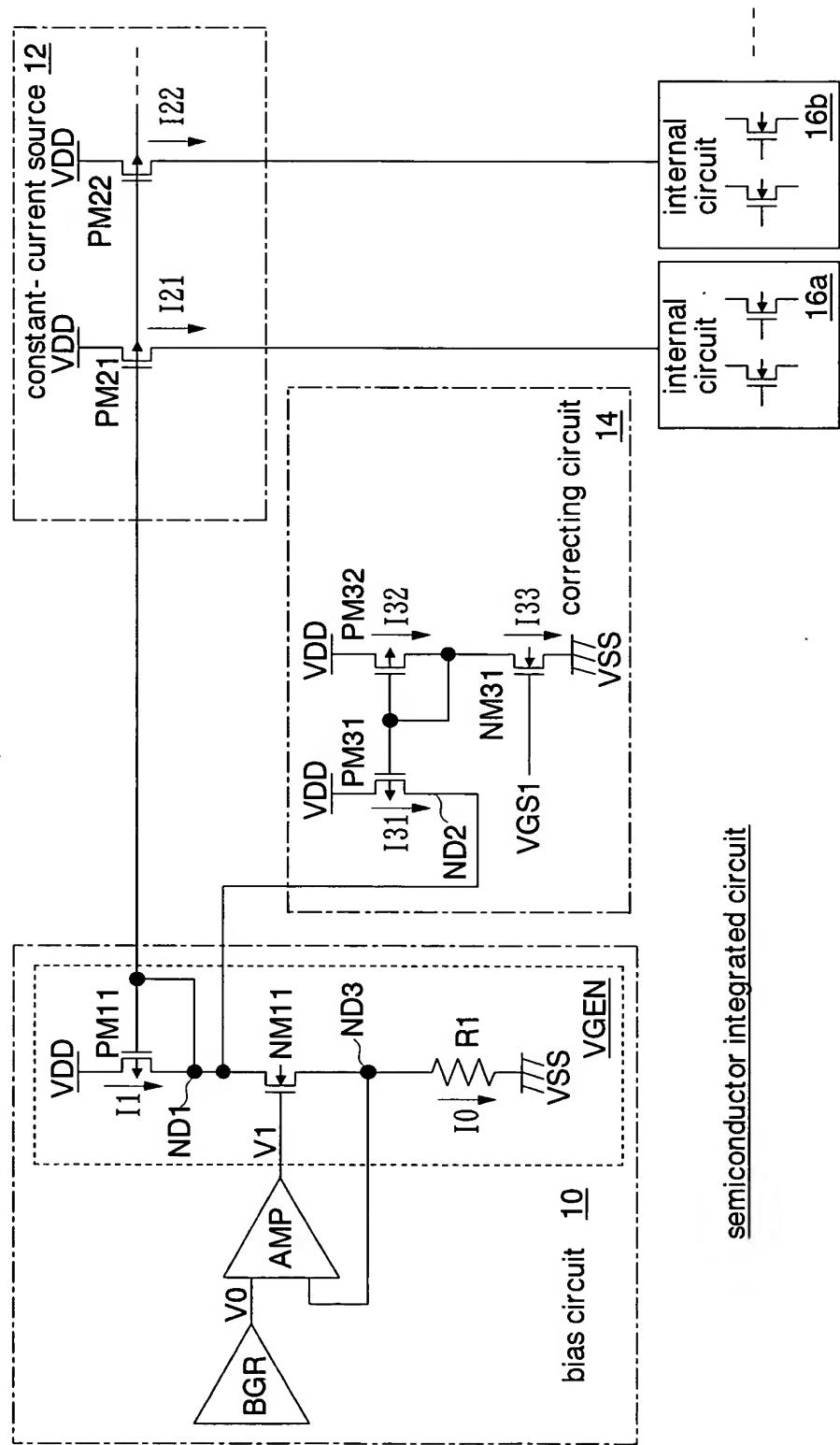


Fig. 4

semiconductor integrated circuit

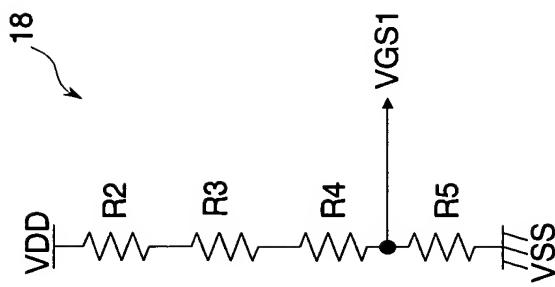


Fig. 5

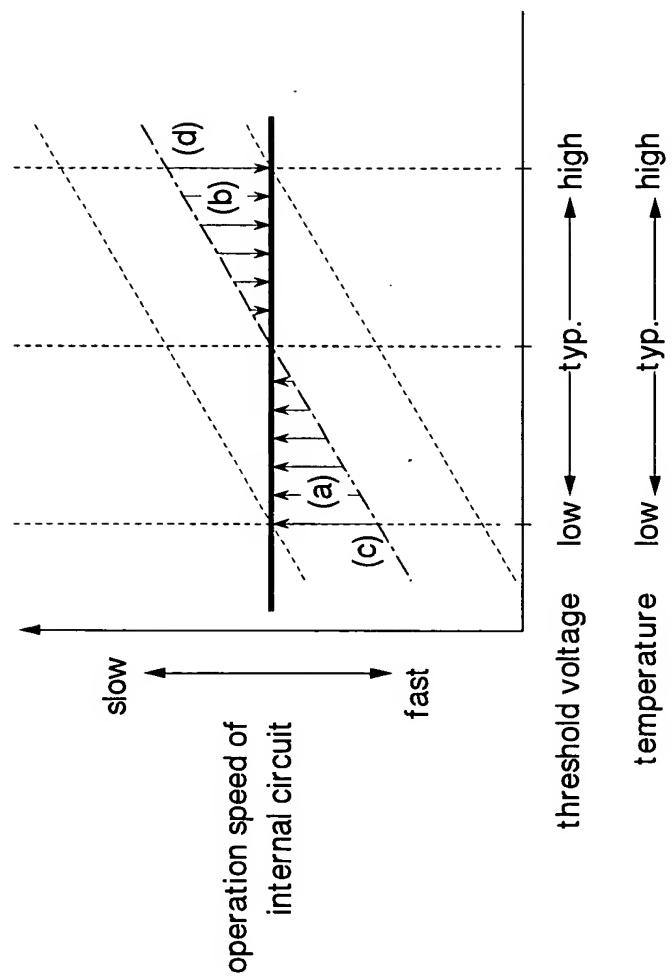


Fig. 6

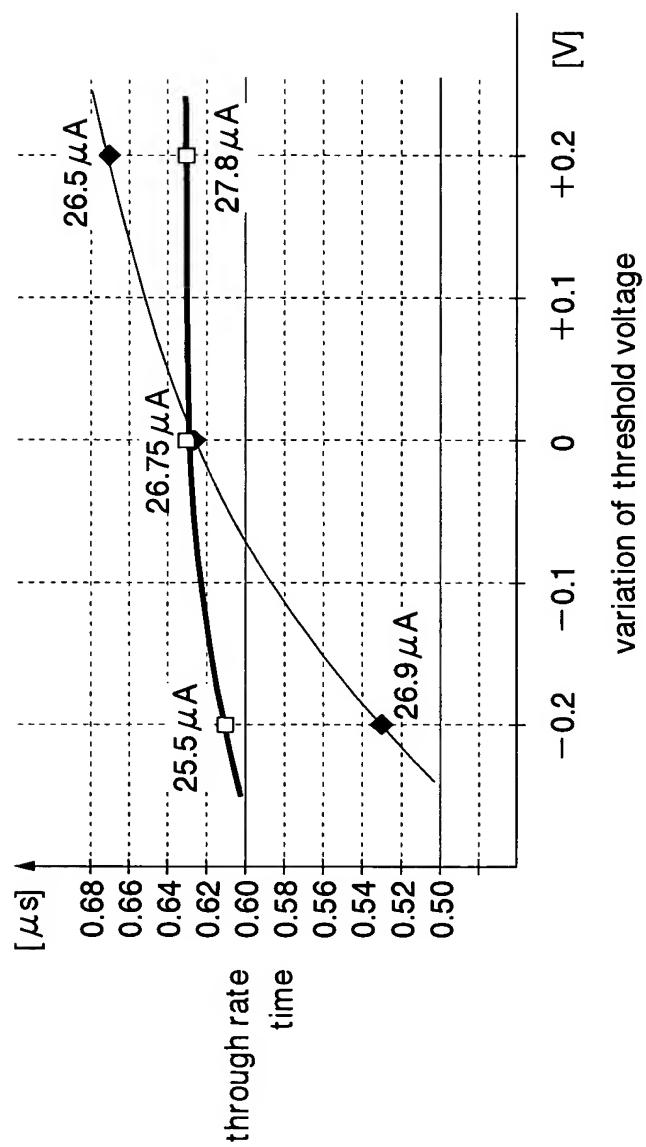


Fig. 7

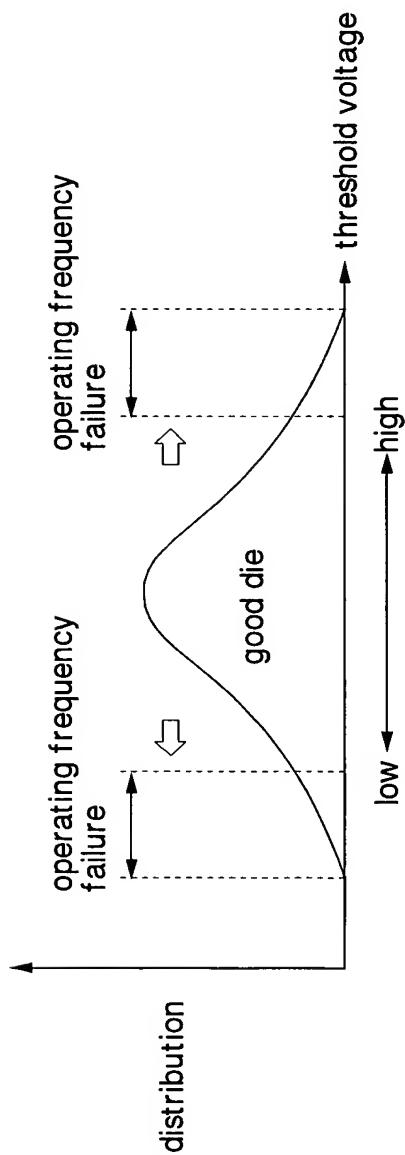


Fig. 8

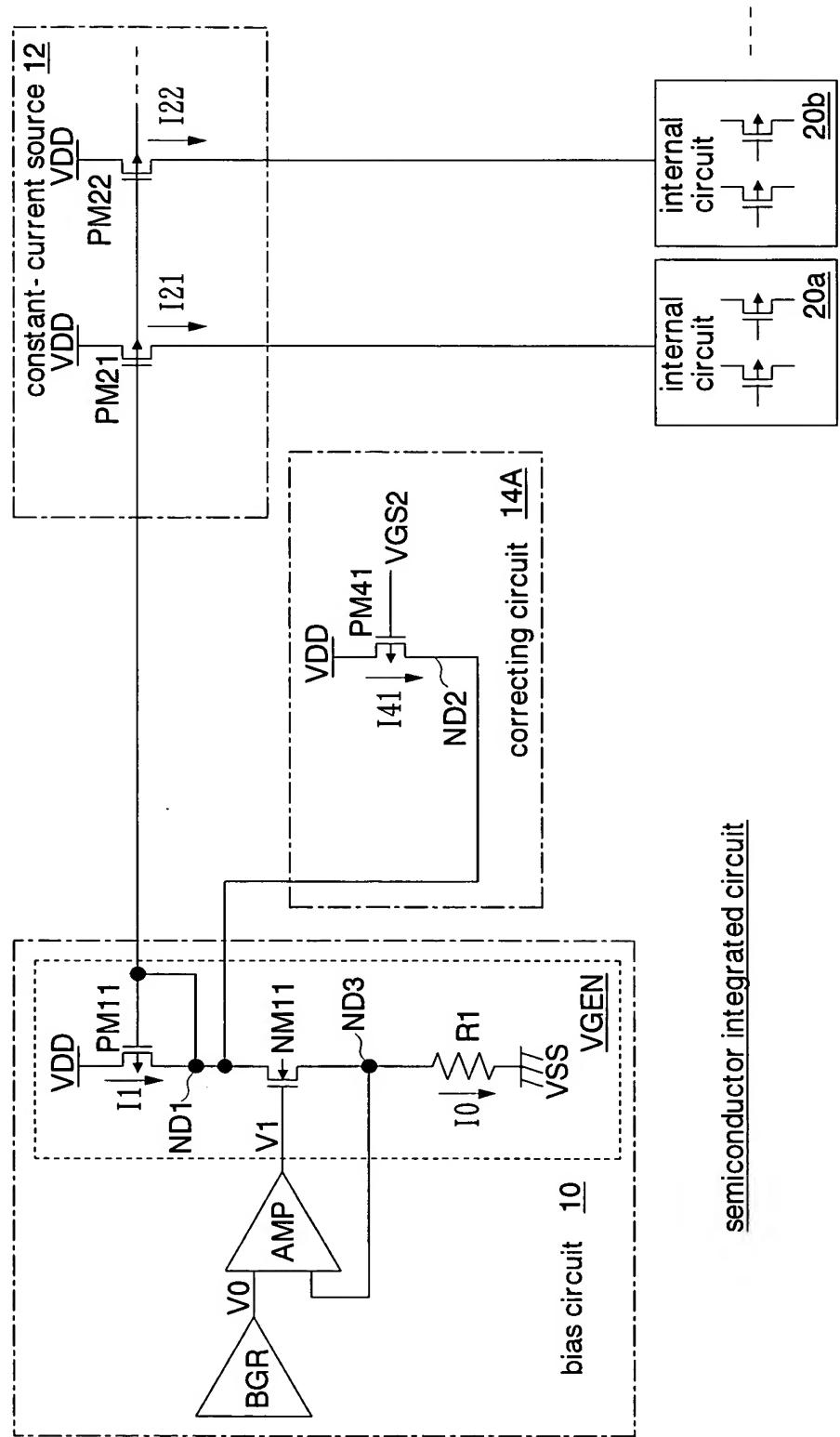


Fig. 9

semiconductor integrated circuit

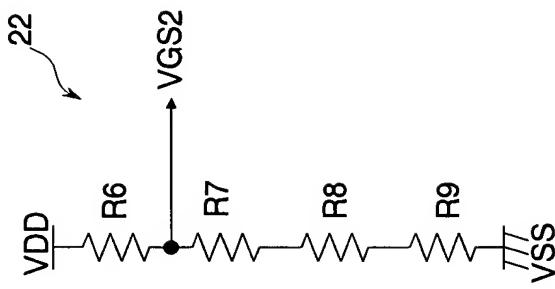


Fig. 10

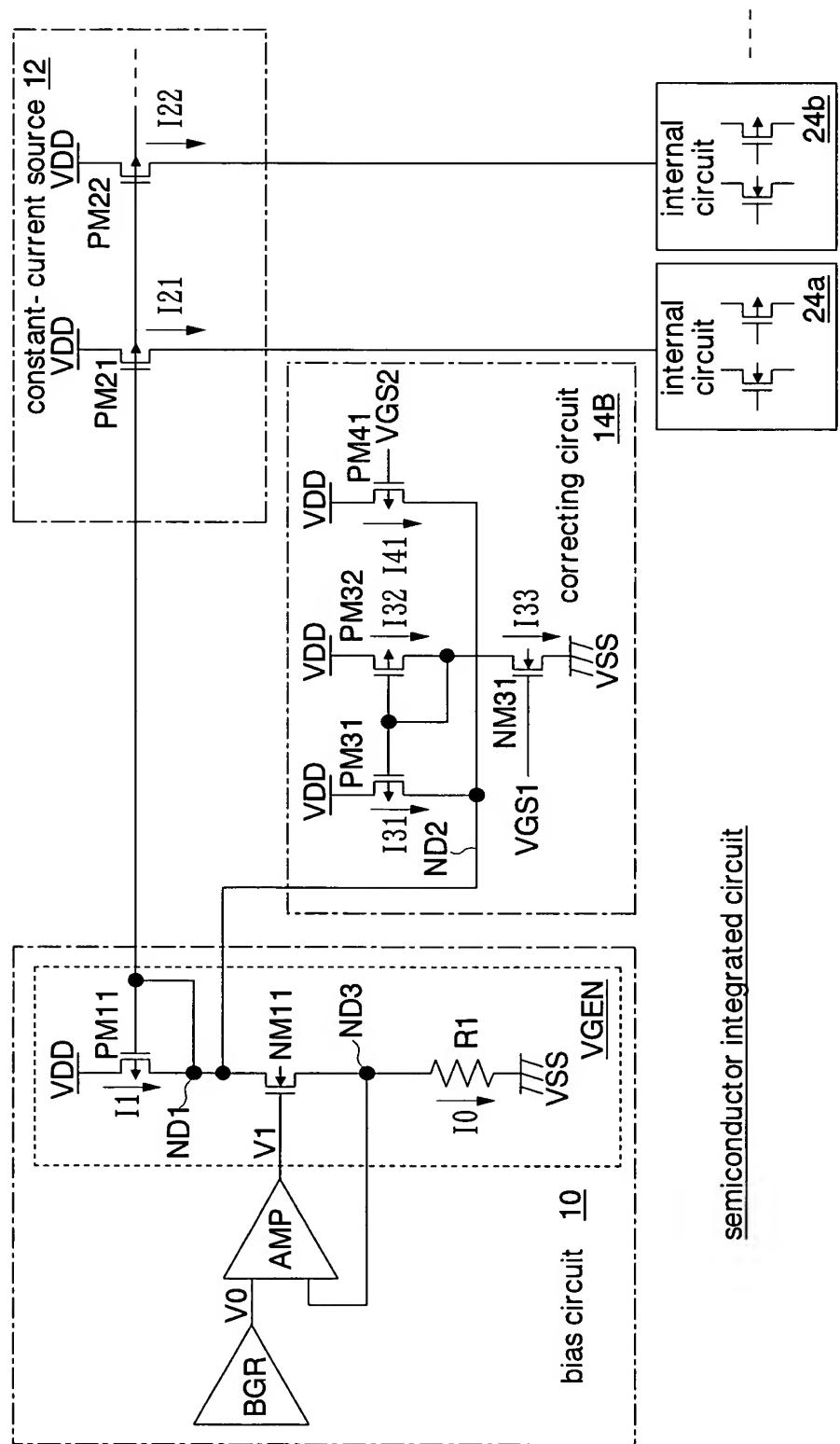


Fig. 11

semiconductor integrated circuit

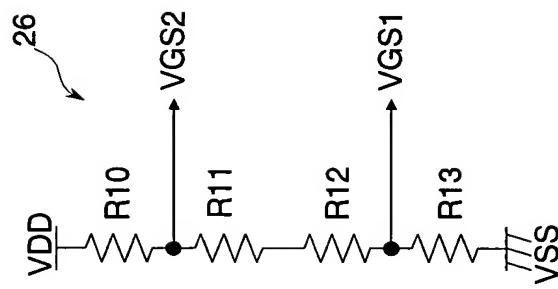


Fig. 12

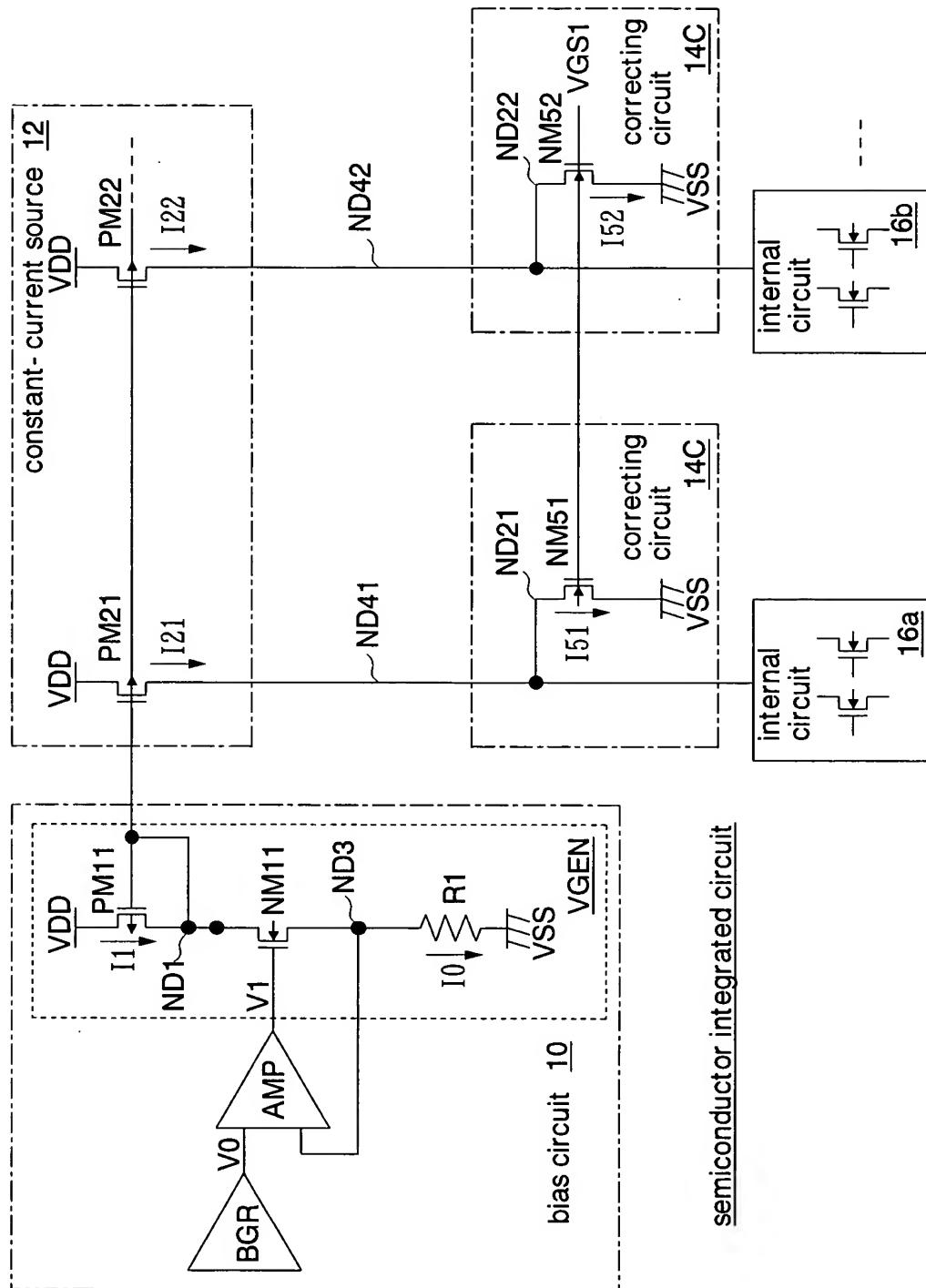


Fig. 13

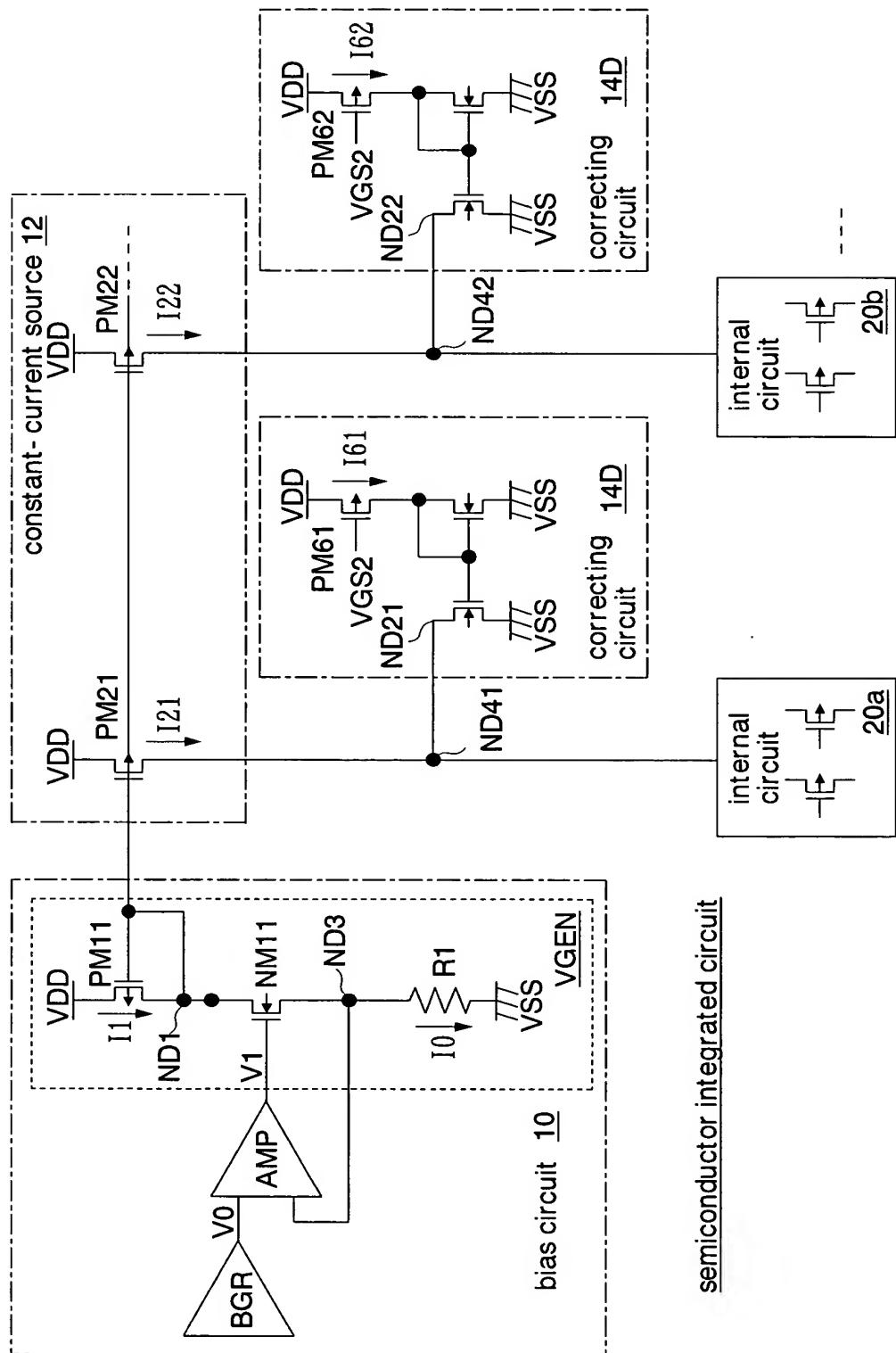


Fig. 14

semiconductor integrated circuit

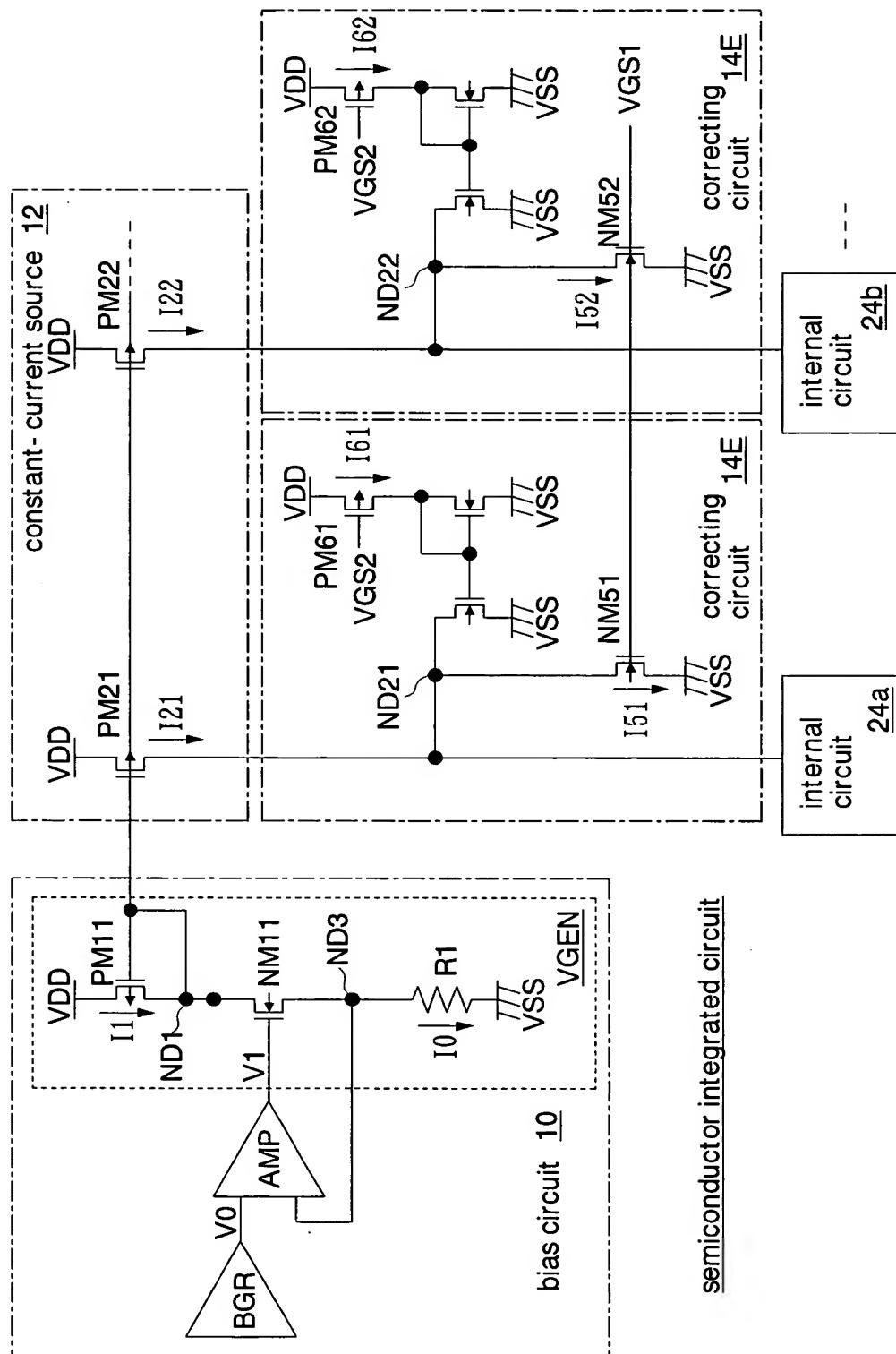


Fig. 15

semiconductor integrated circuit

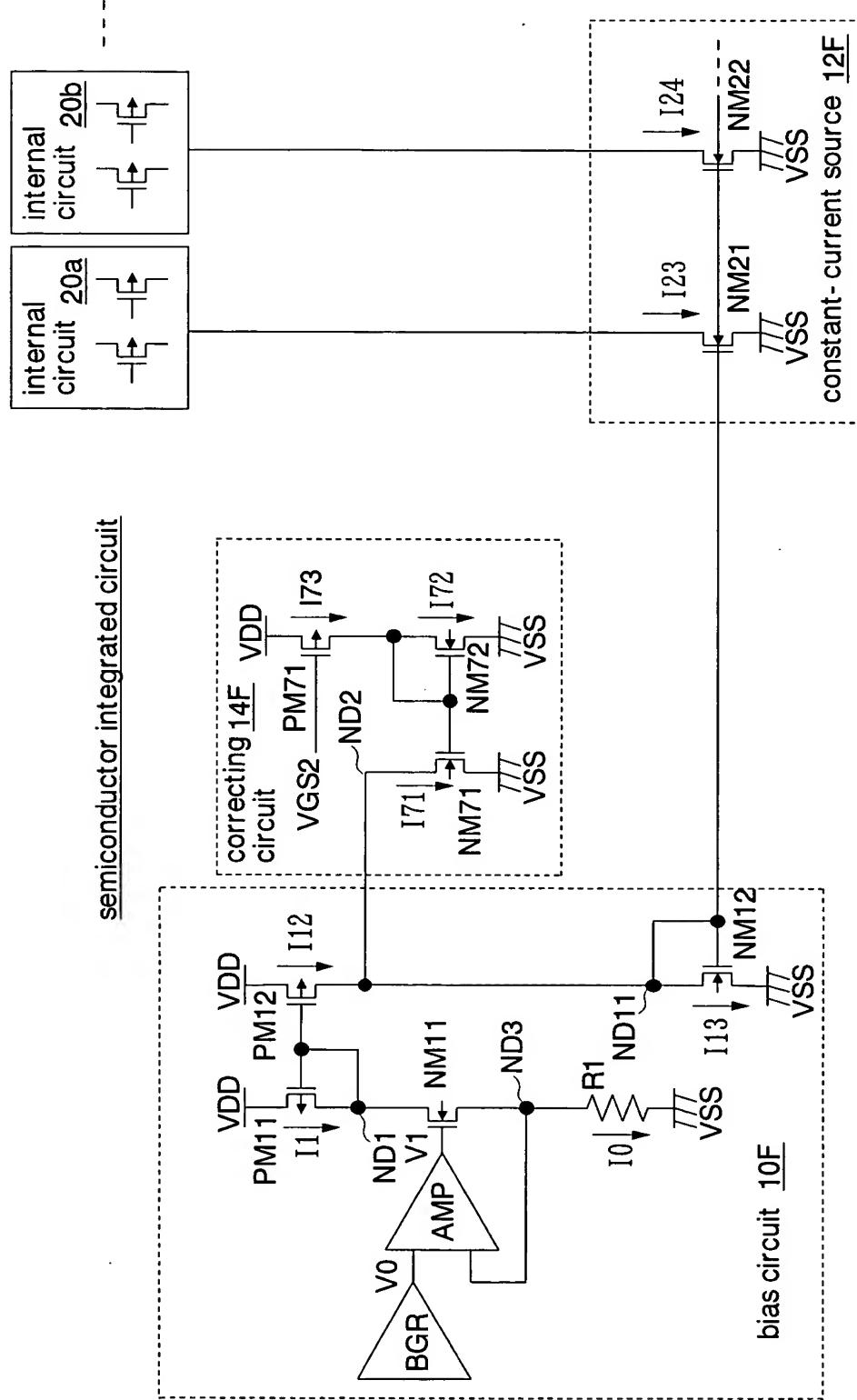


Fig. 16

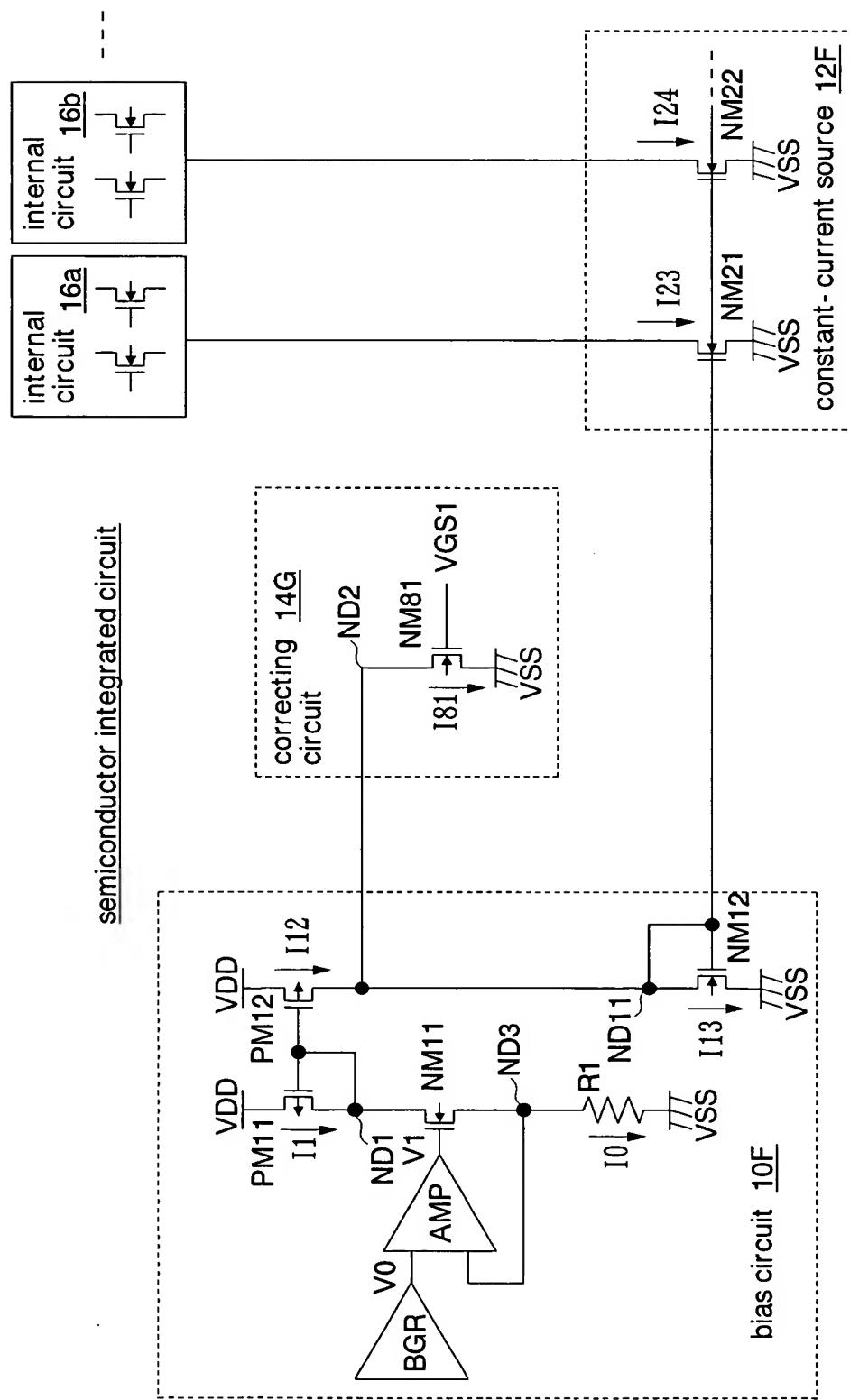


Fig. 17

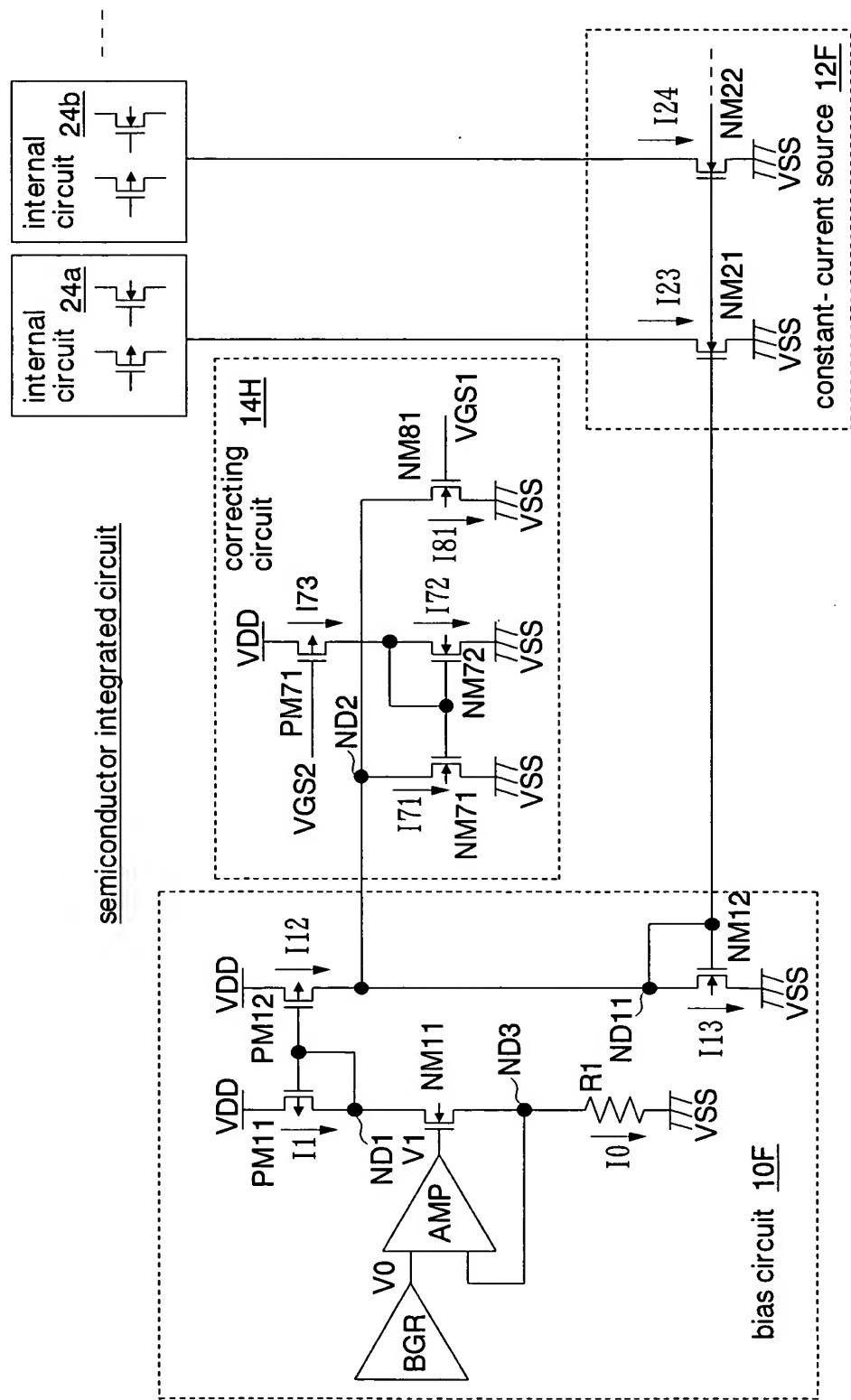


Fig. 18

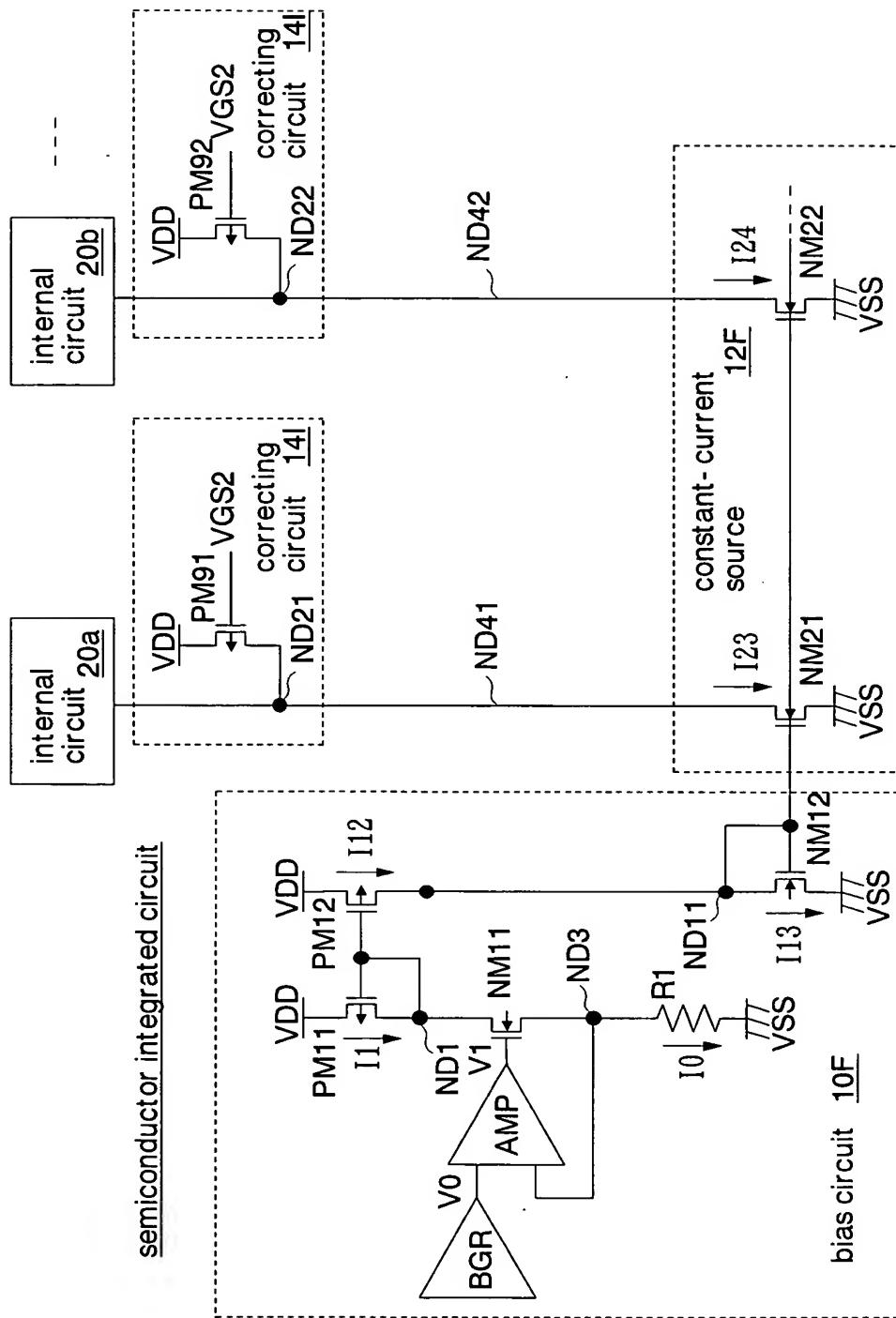


Fig. 19

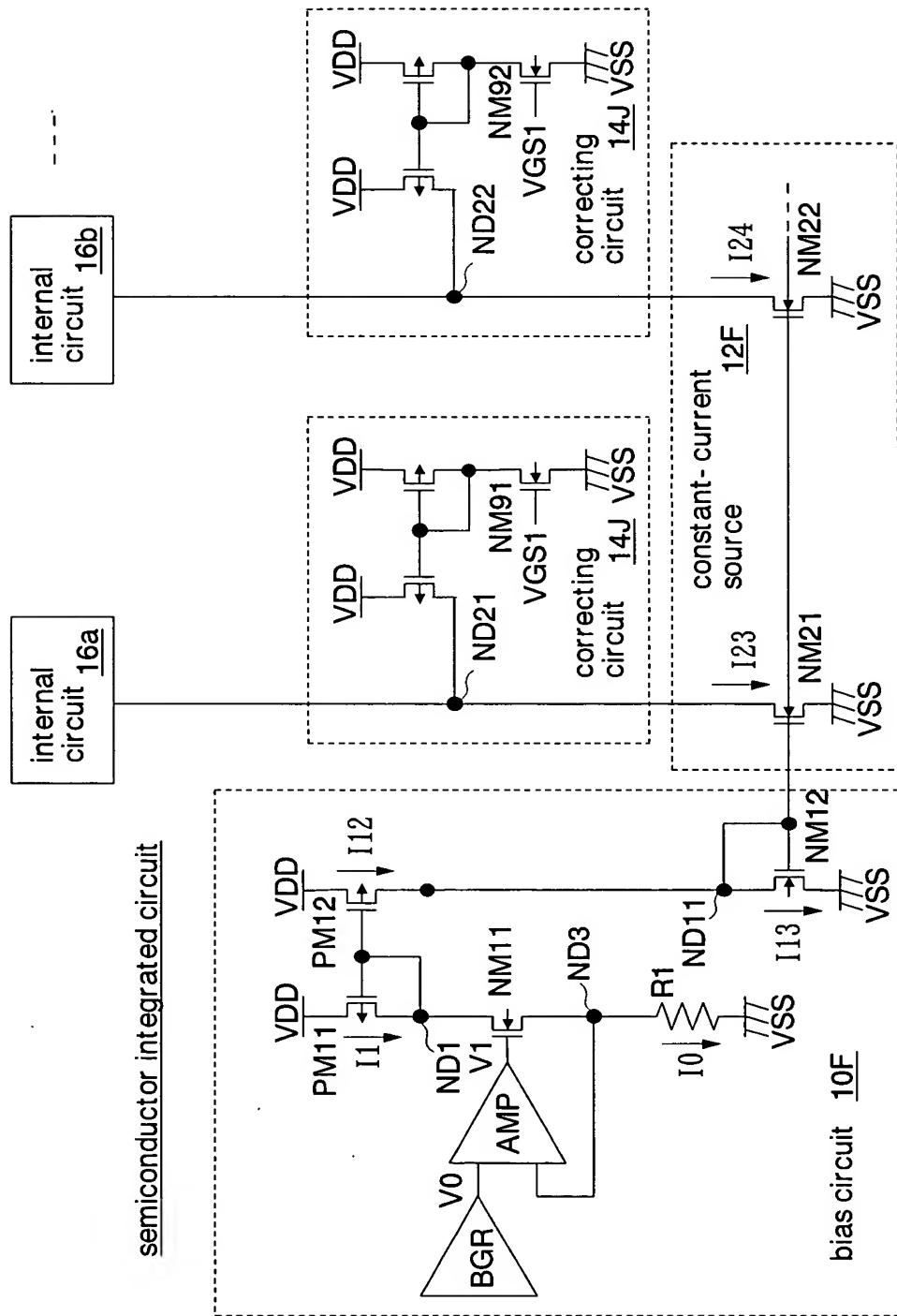


Fig. 20

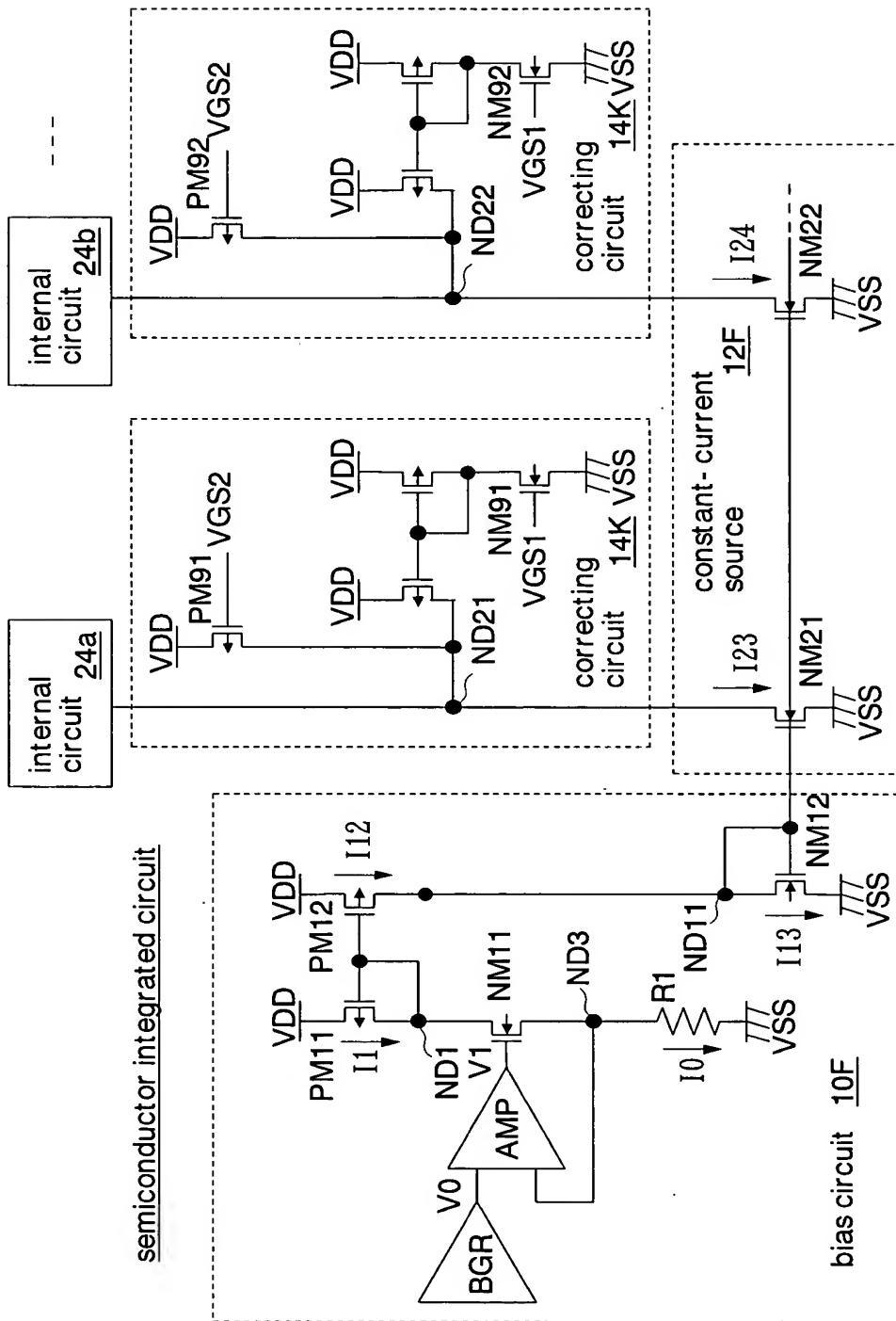


Fig. 21